ORTEC

ORTEC 418A

Universal coincidence

Five selectable coincidence, anticoincidence or disabled inputs, with majority logic to set the required number of coincident signals.



Inputs	Coincidence requirement	Input A resolving time
5 [1]	**1–5 coincident pulses** [2]	**100 ns–2 µs** [1]

Specifications

ELECTRONICS & SIGNAL

Inputs	5	[1]
Input A resolving time	100 ns–2 µs	[1]
Inputs B–E resolving time	Set by input pulse width	[1]
Input voltage	+2…+30 V	[1]
Input pulse width	50 ns to DC	[1]
Input impedance	>1.5 kΩ	[1]
Output	+5 V; 500 ns	[1]

ENVIRONMENT & RATINGS

Coincidence requirement	1–5 coincident pulses	[2]
perating temperature	0–50 °C	[1]

Sources

[1] **418A Universal Coincidence**
RTEC (AMETEK) · datasheet · p. 1, description; p. 1, Specifications – Performance; p. 1, Specifications – Inputs · checked 2026-10-16
https://edge.sitecorecloud.io/ametekinc1236b-ametekb19a-productiondcfa-1d17/media/Project/Ametek/ortec-onlin…

[2] **Model 418A Universal Coincidence Operating and Service Manual (Rev. C)**
RTEC (AMETEK) · manual · p. 11, §2.1 Performance · checked 2026-10-16
https://web.archive.org/web/20231020173618/https://www.ortec-online.com/-/media/ametekortec/manuals/4/418a-m…

Every value is taken from the manufacturer's own document; the number after the value refers to the source list. Always check against the latest datasheet before ordering.

The data comes from the manufacturer's published documents. Manufacturers may change specifications; the current configuration and performance are confirmed in the quotation.